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Applicant(s):	Eliyahou Harari, Daniel C. Guterman and Robert F. Wallace
Assignee:	SanDisk Corporation
Title:	Removable Mother/Daughter Peripheral Card
Serial No.:	UNASSIGNED	Filing Date:	HEREWITH
Examiner:		Group Art Unit:	2154
Docket No.:	11587 M-10214-7C US		

San Francisco, California

COMMISSIONER FOR PATENTS
Washington, D. C. 20231

PRELIMINARY AMENDMENT

Dear Sir:

Please amend the above-identified continuation application being filed herewith, as follows:

IN THE SPECIFICATION:

Page one, rewrite the first paragraph at lines 6 and 7, to read as follows:

This is a continuation of Application Serial No. 09/887,197, filed June 21, 2001, now Patent No. _,___,___, which is a continuation of Application Serial No. 09/241,222, filed February 1, 1999, now Patent No. 6,266,724, which is a continuation of Application Serial No. 08/781,539, filed January 9, 1997, now Patent No. 5,887,145, which is a continuation of Application Serial No. 08/462,642, filed June 5, 1995, now abandoned, which is a continuation of Application Serial No. 08/398,856, filed March 6, 1995, now abandoned, which is a continuation of Application Serial No. 08/151,292 filed November 12, 1993, now abandoned, which is a continuation-in-part of Application Serial No. 08/115,428 filed September 1, 1993, now abandoned. Patent No. 5,887,145 is hereby incorporated herein by this reference.

IN THE CLAIMS:

Cancel all of the original application claims 1-49, and substitute the following new claims therefor:

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50.   (New) A method of storing user data on and retrieving user data from a non-volatile memory card, comprising:

encoding the user data,

storing both the encoded user data and information useful to decode the encoded user data on the memory card,

thereafter reading both the encoded user data and the decoding information from the memory card, and

decoding the read encoded user data by use of the decoding information read from the memory card, thereby to obtain the user data.

51.   (New) The method of claim 50, wherein encoding the user data includes compressing the user data, and the decoding information stored on the memory card includes a decompression algorithm.

52.   (New) The method of claim 50, wherein encoding the user data includes encrypting the user data, and the decoding information stored on the memory card includes a decryption algorithm.

53.   (New) The method of claim 50, wherein encoding the user data includes encrypting the user data, and the decoding information stored on the memory card includes a decompression key.

54.   (New) The method of claim 50, wherein the decoding information includes an algorithm useful to decode the encoded user data.

55.   (New) The method of claim 50, wherein the decoding information includes an key useful to decode the encoded user data.

56.   (New) The method of claim 50, wherein the decoding information includes a software or hardware driver useful to decode the encoded user data.

57.   (New) The method of claim 50, wherein the non-volatile memory card includes a flash EEPROM array, and both the encoded user data and the decoding information are stored in the flash EEPROM array.

58.   (New) The method of claim 57, wherein storing includes programming both the encoded user data and decoding information into individual memory cells of the flash EEPROM array in more than two states, thereby to store more than one bit of said user data and decoding information per cell.

59.   (New) The method of claim 50, wherein the encoding and storing occur when the memory card is electrically connected to a first host system, and wherein the reading and decoding occur when the memory card is electrically connected to a second host system.

60.   (New) The method of claim 59, wherein either the encoding is accomplished by the first host system or the decoding is accomplished by the second host system.

61.   (New) The method of claim 59, wherein at least one of the encoding and decoding are accomplished in a memory controller function included in a mother electronic card that is removably connectable with at least one of the first and second host systems and into which the memory card is removably connectable.

62.   (New) The method of claim 50, wherein at least one of the encoding and decoding are accomplished in a memory controller function included in a mother electronic card to which the memory card is removably connectable.

63.   (New) A method of storing user data on and retrieving user data from a non-volatile memory card, comprising:

connecting the memory card to a first host system,

encoding the user data within the first host system,

storing the encoded user data and information useful to decode the user data on the memory card from the first host system,

removing the memory card from connection with the first host system,

connecting a mother card to a second host system, wherein the mother card includes a controller function for the memory card,

connecting the memory card to the mother card,

thereafter causing the mother card to read the encoded user data and the decoding information from the memory card, and

decoding the read encoded user data within the controller function of the mother card by use of the decoding information read from the memory card, thereby to provide the user data to the second host.

64. (New) The method according to claim 63, wherein the first host system includes a camera and the user data includes visual field data obtained by the camera.

65. (New) The method according to claim 64, wherein the second host system includes a personal computer.

66. (New) A method of storing user data on and retrieving user data from a non-volatile memory card, comprising:

connecting a mother card to a first host system, wherein the mother card includes a controller function for the memory card,

connecting the memory card to the mother card,

encoding user data provided by the first host system within the mother card controller function,

storing on the memory card the encoded user data and information useful to decode the user data,

removing the memory card from connection with the mother card,

thereafter connecting the memory card to a second host system without use of the mother card,

thereafter causing the second host system to read the encoded user data and the decoding information from the memory card, and

decoding the read encoded user data with the second host by use of the decoding information read from the memory card, thereby to obtain the user data.

67. (New) A non-volatile memory card, comprising:

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a flash EEPROM array,

encoded user data stored in a first portion of the array, and

data of information useful to decode the encoded user data stored in a second portion of the array.

68.   (New) The memory card of claim 67, wherein the stored encoded user data includes compressed user data, and wherein the information useful to decode the stored encoded user data includes a decompression algorithm.

69.   (New) The memory card of claim 67, wherein the stored encoded user data includes encrypted user data, and wherein the information useful to decode the stored encoded user data includes a decryption algorithm.

70.   (New) The memory card of claim 67, wherein the stored encoded user data includes encrypted user data, and wherein the information useful to decode the stored encoded user data includes a decryption key.

71.   (New) A memory system card, comprising:

a connector adapted to be received by a host system,

a receptacle adapted to receive a memory card that includes non-volatile memory,

a controller for programming data into and reading data from the non-volatile memory in response to commands from a host system,

an encoder of data received from a host system, thereby causing encoded data to be stored in the non-volatile memory, and

a decoder of encoded data read from the non-volatile memory, thereby causing decoded data to be provided to a host system.

72.   (New) The memory system card of claim 71, wherein the encoder functions to compress the data received from a host system, and the decoder functions to decompress the encoded data read from the non-volatile memory.

73.   (New) The memory system card of claim 71, wherein the encoder functions to encrypt the data received from a host system, and the decoder functions to decrypt the encoded data read from the non-volatile memory.

74.   (New) The memory system card of claim 71, wherein the decoder operates with information of a decoding algorithm read from a memory card connected with the system card receptacle.

75.   (New) The memory system card of claim 71, wherein the decoder operates with a key read from a memory card connected with the system card receptacle.

REMARKS

By this Preliminary Amendment, the cross-reference at the beginning of the Specification to related parent applications is being updated, and a new set of claims is being substituted for the claims of the original parent application. The new set of claims is directed to the use of daughter memory cards and mother controller cards in the encoding and decoding of data stored on the memory cards. The subject matter of the new claims is described in the application specification, primarily in the following portions: Page 6, lines 8-11; page 10, lines 3-28; and page 26, line 14 to page 29, line 26.

A prompt examination and allowance of the present continuation application are respectfully requested.

<table>
<tr><td>**EXPRESS MAIL LABEL NO:**

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Gerald P. Parsons (signature)
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Jan. 15, 2002
Date</td></tr>
</table>

DETAILS OF AMENDMENT BEING MADE TO THE SPECIFICATION

The paragraph on page 1, lines 6-7, is being rewritten as follows:

This is a continuation of Application Serial No. 09/887,197, filed June 21, 2001, now Patent No. _,___,___, which is a continuation of Application Serial No. 09/241,222, filed February 1, 1999, now Patent No. 6,266,724, which is a continuation of Application Serial No. 08/781,539, filed January 9, 1997, now Patent No. 5,887,145, which is a continuation of Application Serial No. 08/462,642, filed June 5, 1995, now abandoned, which is a continuation of Application Serial No. 08/398,856, filed March 6, 1995, now abandoned, which is a continuation of Application Serial No. 08/151,292 filed November 12, 1993, now abandoned, which is a continuation-in-part of Application Serial No. 08/115,428 filed September 1, 1993, now abandoned. Patent No. 5,887,145 is hereby incorporated herein by this reference.